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KG Blemel - US Patent App. 09/977,140, 2001 - Google Patents

... comprised of computing devices that use computer algorithms embodied in **software** programs to ... FPGA and non-volatile memory, digitizers, and other elec- tronic **hardware** described in ... controller

8. Program addresses are accomplished by an address **partitioning** and control ...

The Specification Language SpecC within the PARADISE Design Environmentpsu.edu [PDF]

A Rettberg, FJ Rammig, A Gerstlauer, DD ... - ... and Design of ..., 2001 - books.google.com

... [Ha95] W. Hardt, "An Automated Approach to HW/SW-Codesign". In IEEE Colloquium: **Partitioning** in **Hardware-Software** Codesigns. London, Great Britain, February 1995. [HaReK199] W. Hardt, A. Rettberg, B. Kleinjohann, "The PARADISE design environment". In Proc. ...

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M O'Neill, M Price, RJ Richter, S Shah, G Entine, J King ... - 1990 - oai.dtic.mil

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CJ Price - 1999 - books.google.com

... Consider, for example washing machines, cars, aeroplanes, computers, **software**. These products are too costly to be "disposable". ... The Web address for obtaining **software** and other material linked to the book is "http://www.aber.ac.uk/~cjp/diagnosticbook/". ...

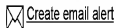
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NCS Handbook - JAERI-Review, 2001 - dspace.lboro.ac.uk

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Finite State Machine Labs. □. Florida State University ...
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www.ctit.utwente.nl/library/proceedings/wp0908.pdf

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Despite this inherent simplicity, the **use case** approach is frequently ... of the relevant concepts including traditional **finite state machines**, ... Executable UML: A Foundation for Model-Driven **Architecture** ... This is the first book on embedded systems to offer a unified approach to **hardware** and **software** ...
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V Bahel, H Millet, M Hickey, H Pham, GP ... - US Patent ..., 1995 - Google Patents

... When new features are added, although implemented largely in **software**, frequently the existing **hardware** will also ... Moreover, the distributed **architecture** allows operating functions to be distributed over the plural-ity of microprocessors so that a portion of the system (such ...

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BG Ruth, ARMY RESEARCH LAB ABERDEEN ... - 2001 - oai.dtic.mil

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DB Whitney, TM Katanik, AIRSPACE TECHNOLOGY ... - 1987 - oai.dtic.mil

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SD Brunn - GeoJournal, 1998 - Springer

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... To enable designers to rapidly evaluate different architectural templates and partitionings, Chinook facilitates migration of functionality among processing elements and ... Constrained software generation for HW-SW systems. ... The Verilog Hardware Description Language. ...

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A Girault, B Lee, EA Lee - IEEE Transactions on Computer- ..., 1999 - ieeexplore.ieee.org

... berkeley.edu). Publisher Item Identifier S 0278-0070(99)03963-9. scheduling, and reasonably efficient synthesis of embedded software or hardware. ... Finite state machines (FSM's) have long been used to describe and analyze intricate control sequences. ...

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... Depending on the selected **architecture**, each task will take a specified number of clock to be executed (one clock ... depending on the selected target processor for **software**) and in different execution constraint (concurrency for **hardware**, mutual exclusion for **software**). ...

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Hierarchical adaptive state machine for emulating and augmenting software

MC Heimlich, KRS Hilaire - US Patent 5,903,886, 1999 - Google Patents

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C D ... process of designing the **hardware** and **software** components Placement is ...

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System specification with the SpecCharts language

S Narayan, F Vahid, DD Gajski - IEEE Design and Test of Computers, 1992 - computer.org

... to invoke a high-level synthesis tool for each chip's description or to translate to **software** for execution on ... as memories), offers several advantages. One is that the designer can make important **architectural** decisions (bus ... The two channels are **hardware** description language. ...

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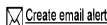
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N Medvidovic, RN Taylor - IEEE Transactions on software engineering, 2000 - Citeseer

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... 9. A. Krüger, "Interface **design** for Time-Triggered Real-Time System Architectures", Ph.D ... 10. A. Krüger, H. Kopetz, "A **Network** Controller Interface for a Time-Triggered Protocol ... SAE Symposium on Future Transportation Electronics: Multiplexing and In-Vehicle Networking, SAE ...

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... the development of an **architecture** is based on a control circle between **design** and evaluation. ... there is also a mutual interdependency between the physical construction of the **vehicle** and the ... This platform, called Agent **Network** System ANTS, is based on agent technology. ...

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... This case study presents one of the **design** concepts of the brake, steering and throttle systems. ...
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E Touloupis, JA Flint, D Ward - ... Systems and Control Division Research 2003 - Citeseer

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Development Guidelines for **Vehicle Based Software**, The Motor Industry **Software** ...

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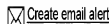
[Computer-aided architecture design & optimized implementation of distributed automotive EE systems](#)

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A Rajnak, A Kumar - 44th ACM/IEEE Design Automation ..., 2007 - [ieeexplore.ieee.org](#)

... We detail an already-deployed point tool in our flow, the **Network Cluster Builder**. ... to the AUTOSAR
standard to improve its applicability to system-level **design**. ... **User-visible vehicle** functionality is
implemented through interconnected application **Software** Components (SWCs). ...

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S Grange, T Fong, C Baur - IEEE International Conference on Robotics ..., 2000 - Citeseer
 ... 4 System **design** 4.1 Overview ... The **user** interface is connected to the rest of the system via a persistent **network** link. Base Station. ... Robot. The teleoperated **vehicle** is equipped with **on-board** sensors (including a pan-tilt camera) and a motion controller. ...
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[User and operator perspectives in transit network design](#)

A Ceder, Y Israeli - ... Journal of the Transportation Research Board, 1998 - Trans Res Board
 ... It is a histogram describing the number of passengers **on board** the transit **vehicle** versus the transit ... to incorporate three operational planning ele- ments simultaneously at the macro and micro levels: **network design**, frequency (timetable) setting, and **vehicle scheduling** ...
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[Deploying a wireless sensor network on an active volcano](#)[psu.edu](#) [PDF]

G Werner-Allen, K Lorincz, M Welsh, O ... - IEEE Internet ..., 2006 - computer.org
 ... Before describing our **design** in detail, let's take a high-level view of our sensor node ... The MSP430 microcontroller provides **on-board** ADCs, but they're unsuitable for our application. ... A pair of alkaline D cell batteries powered each sensor node — our **network's** remote location ...
 Cited by 271 - Related articles - All 15 versions

[\[PDF\] Design guidelines for safety of in-vehicle information systems](#)[transport-research.info](#) [PDF]

A Stevens, A Quimby, A Board, T ... - Prepared for the ..., 2002 - transport-research.info
 ... Section 2 describes the different stages of the **design** process and considers briefly what each ...
 Section 3 deals with the documentation and **user** instructions that may need to be provided ... 4 provides guidelines (if required) about how the IVIS should be fitted within the **vehicle**. ...
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D Siewiorek, A Smaligic, L Bass, J Siegel, R ... - Proceedings of the 2nd ..., 1998 - Citeseer
 ... The **on-board vehicle** system prototype gathers internal status data from embedded sensors in systems such as propulsion and doors. ... Page 3. impact on the overall **design**. ... FarSite requires a Winsock compliant Windows **network** interface, which is available with the WaveLAN. ...
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[An autonomous guided vehicle for cargo handling applications](#)

HF Durrant-Whyte - The International Journal of Robotics ..., 1996 - ijr.sagepub.com

... of the overall **vehicle** and **on-board** systems architecture; the development of a navigation system appropriate for large, outdoor, high-speed vehicles; and the experimental program leading to validation of the **vehicle** system. 1. Introduction This article describes the **design** of an ...
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[PDF] Robotic ocean vehicles for marine science applications: the european asimov project

uac.pt [PDF]

A Pascoal, P Oliveira, C Silvestre, L Sebastião ... - OCEANS- ..., 2000 - horta.uac.pt
... The **vehicle** is a major re-**design** of the MARIUS AUV, developed under the MAST-I and ... will be compiled, downloaded to, and run in real time on the computers installed **on-board** the ASC ...
viewing all the modules inside each one of the vehicles as nodes in a local area **network** ...
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On-board network for motor vehicles

A Moller, H Reimann, B Schurmann, G Wilts - US Patent 4,942,571, 1990 - Google Patents
... These harnesses are of very complex **design** and are prone to manufacturing errors since the fabrication of a harness is done predominantly manually. ... It has therefore been proposed already to replace the wire-harness of a motor **vehicle** by an **on-board network**, in which ...
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Design of automatic guided vehicle systems

WL Maxwell, JA Muckstadt - IIE Transactions, 1982 - informaworld.com
... accomplished using some type of guide path in or on the floor, **on-board** computers or ... a system and wants to determine how many vehicles are needed to support that **design**. ... Only the issue of **vehicle** requirements must be transportation **network** corresponding to this problem is ...
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A large-scale complex virtual environment for team training

TW Mastaglio, R Callahan - Computer, 1995 - ieeeexplore.ieee.org
... Potential applications are shared **design** and other cooperative activities, long-distance telecommunications or ... a combat **vehicle** simulator, the crew has the same **vehicle** and **on-board** weapons controls and ... data packets transmitted over the FDDI LAN from all **network** entities. ...
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... To enable designers to rapidly evaluate different **architectural** templates and partitionings, Chinook facilitates migration of functionality among processing el- ements and ... Constrained **software** gen- eration for HW-SW systems. ... The Verilog **Hardware** Description Language. ...

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... architec- ture platform must be abstracted at a level where the application **software** can use a high- level interface to the **hardware**: the API ... A **software** layer performs this abstraction, as shown in Figure 1 (next page ... This layer wraps the essen- tial parts of the **architecture** platform: ...

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... 226 5.4 The **architecture** selection user interface. ... Lavagno then became the key architect of the POLIS system. ... xviii **Hardware-Software** Co-design of Embedded Systems ■ Cadence, and in particular, Patrick Scaglia, Vice-President of Research, Jim Rowson, Fellow, of the Aha ...

Cited by 768 - [Related articles](#) - [BL Direct](#) - All 3 versions**A model and methodology for hardware-software codesign**DE Thomas, JK Adams, H Schmit - IEEE Design and Test of ..., 1993 - [computer.org](#)

... In a **software** imple mentation, memory bandwidth is limited to that supported by the CPU and memory subsystem. A **hardware** implementation can achieve high memory bandwidth at low cost by employing a memory **architecture** tailored to the applica- tion. ...

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... The Echelon Neuron architecture also uses a medium-performance serial link between the processors. ... Embedded software can usually be thought of as a system of communicating processes, though the ... a sequential machine, which we will use to refer to either a hardware or ...
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Á Lédéczi, A Bakay, M Maroti, P Volgyesi, G ... - Computer, 2001 - ieeexplore.ieee.org
... GME's most important distinguishing characteristic is the consistent application of a metalevel architecture enforcing the ... (computer hardware and software ... Various research groups have developed environments exclusively targeting embedded software modeling formalisms ...
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WH Mangione-Smith, B Hutchings, D Andrews, A ... - Computer, 1997 - ieeexplore.ieee.org
... refining the emerging architectures, producing more effective software/hardware APIs, better ... for application development that incorporate the models of hardware reconfiguration, and ... Minsky and A. DeHon, "MATRIX: A Reconfigurable Computing Architecture with Configurable ...
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[PDF] A classification and comparison framework for software architecture description languages

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N Medvidovic, RN Taylor - IEEE Transactions on software engineering, 2000 - Citeseer
... It is a mechanism for abstracting and reusing component interface elements. ... 5. As MetaH is used to specify both the software and the hardware architecture of an application, system is a hardware construct, while application pertains to both. Page 16. 16 ...
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[PDF] A formal approach to reactive system design: unmanned aerial vehicle flight management system design example

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TJ Koo, B Sinopoli, A Sangiovanni-Vincentelli, S ... - Proc. of IEEE ..., 1999 - Citeseer
... Depending on the selected architecture, each task will take a specified number of clock to be executed (one clock ... depending on the selected target processor for software) and in different execution constraint (concurrency for hardware, mutual exclusion for software). ...
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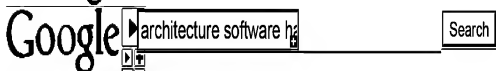
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of co-design **finite state machines** (CFSMs). SHIM [8] has the most similar features ... **Interface** Format (SHIFT) for **hardware/software interface** modules. ... **architecture** information, for example, the type of CPU and operating system. ...

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4. [Hardware/Software Communication and System Integration for ...](#)

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Using this scheme, the channel adapter is in fact a **finite state machine** that **Interface**. Controller. Figure 15. **Architecture**

template with **hardware** I/O unit **architecture** may still need to be implemented. In this case, we **auto-** ...

www.springerlink.com/index/k8747x2lv3016566.pdf

5. **Software** development process - Wikipedia, the free encyclopedia

... teams to **interface** with customer/field teams testing the **software** to identify any real ... Design and **architecture** emerge out of refactoring, and come after coding. ... to solving **software** (and **hardware**) problems at the requirements, ... Finite state machine (FSM) based methodologies allow executable **software** ...

en.wikipedia.org/wiki/Software_development_process

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by Y Liu - 2008 - Related articles

software and **hardware**. CNC system is demanded to have modular structure and ability of fast library provides **interfaces** to define and modify **FSM**. ...

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7. [PDF] Generating Mixed **Hardware/Software** Systems from SDL Specifications

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of modern **automotive** systems makes SDL also feasible for

Figure 2: **Hardware architecture** of a EFSM. Finite-State. Machine.

Interface **Hardware**. **Software**. Reg. Reg. ISR. ISR. **FSM** go fin.

Figure 6: HW/SW-**Interface** ...

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hardware software codesign. The target board contains a. TMS320C30 DSP processor and up ... personal communication, **automotive** control and consumer electronics. Since various processing blocks ... Numerous work [1-8] on codesign system **architectural** codes which contain both control **FSM** and **interface** circuitry. ...

doi.ieeecomputersociety.org/10.1109/ISS.1998.730601

9. NSF Workshop on Logic Control for Manufacturing Systems

Finite State Automata (State Machines), Timed State Automata, and Petri Nets will be ... PCs provide users with computational and human/**machine-interface** ... and generic PC/PLC-based **software/hardware architecture** for the control of FMCs. The **automotive** industry demands large quantities of parts to be produced ...
www-personal.umich.edu/~tilbury/logiccontrol/abstracts.html
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C ESCA - Proceedings, 1996 - IEEE Computer Society Press

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JD McGregor - 2001 - Citeseer

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July 30-August 4, 2000: Santa Barbara, ...

Q Li - 2000 - IEEE Computer Society

[\[BOOK\] Doing hard time: developing real-time systems with UML, objects, frameworks, and patterns](#)

BP Douglass - 1999 - books.google.com

... Lau, The An of Objects: Object-Oriented Design and **Architecture** LeffingwellAVidrig, Managing ... Process for Specifying Component-Based **Software** Szyperski, Component **Software**, 2E: Beyond ... Resource-Limited Target Environments 82 2.11 Low-Level **Hardware** Interfacing 83 ...

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H Van Vliet, H Van Vliet, JC Van Vliet - 1993 - Citeseer

... 291 11.5 **Software Architecture** Assessment **Software** was mostly developed by **hardware** vendors and given away for free. ... Sometimes, **software** can be used as a **vehicle** for new technologies, such as typesetting, the production of chips, or manned space trips. ...

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VI Nagasamy, N Berry, C Dangelo - IEEE Design and Test of ..., 1992 - computer.org

... The VHDL simulator helps the user verify the **design** at different stages. During initial **specification**,

it verifies whether the input VHDL description meets the IEEE DESIGN & TEST OF COMPUTERS ...

62 IEEE DESIGN & TEST OF COMPUTERS Page 6. the architecture: Goals ...

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M Gunzert, A Nägele - pdse, 1999 - computer.org

... If there is not enough **electrical** power available for additional functions (ABS) the signal NOABS is emitted. ... Table 2: mBrake – input and output signals 6.1 Specification of the Observers ... behave fail-silent. $\alpha(\sim\text{FSUREADY} \Rightarrow \text{FAILSILENTMODE})$ 6.2 Internal Design of mBrake ...

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JO Hamblen, H Owen, S Yalamanchili, B ... - Computer architecture ..., 1996 - portal.acm.org

... four hour, one quarter, courses taken in sequence by computer and **electrical** engineering students ...

and benchmark programs are being developed, students **design** the data path architecture of

a ... A written report or **design specification** describing the instruction set and data path ...

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A Krikelis, CC Weems - Computer, 1994 - ieexplore.ieee.org

... Lewin, DW, "Introduction to Associative Processors," Computer Architecture, AK Boulaye and

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systems, especially for real-time applications. ... American Institute of Electrical Engineers. ...

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Besides further emphasis on AHDL, as the main language for **design specification**, a further ... these

areas to the students studying digital system **design**, computer **design**, and related ...

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DD Gajski, L Ramachandran - IEEE Design & Test of ..., 1994 - ieeeexplore.ieee.org

... a rough block diagram of the chip **architecture**, which serves as a pre-liminally, incomplete **specification**. In some cases, the **architects** further refine this initial block diagram before ... into control steps, given the set of resources Scheduling: the **partitioning** of **design** behavior into ...

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K Öner, LA Barroso, S Iman, J Jeong, K ... - Proceedings of the ..., 1995 - portal.acm.org

... Department of **Electrical Engineering** - Systems University of Southern California Los Angeles, CA 90089 ... Whenever a condition is found to violate the protocol **specifications** in a controller ... workloads to determine the most promising designs, under the shortest possible **design** ...

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An investigation into the use of hardware-in-the-loop simulation testing for automotive electronic control systems

IR Kendall, RP Jones - Control Engineering Practice, 1999 - Elsevier

... Once testing begins, every time an issue is found, it must be communicated to the supplier, **design** changes made to ... Fig. 1. The XK8 body control system **architecture**. ... In addition to the **electrical** signals, the real-time program must also have the ability to send and receive SCP ...

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YL Lin - ACM Transactions on Design Automation of Electronic ..., 1997 - portal.acm.org

... 6.5 Controller Issues Traditionally, the **control unit specification** is generated after the datapath is synthesis completed. ... The System **Architect's** Workbench from CMU [Thomas et al. ... Recently, HLS has been used in an embedded system **design** environment. ...

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G Koch, U Weinmann, U Kubschull, W ... - Microprocessors and ..., 1996 - Elsevier

... The **design** flow addressed by COBRA starts with a system level **specification** of a ... J. Hughes, FPGA based AS1C **Hardware** Emulator Architectures, School of **Electrical** and Computer ... 4th Eurochip Workshop on VLSI **Design** Training, Toledo, Spain, September 29/October 1, 1993 ...

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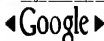
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... These improvements do not require alternative wiring harness changes and do not affect ... micro-
controller 8. Program addresses are accomplished by an address partitioning and control ... with
slave serial and selectMAP functions for multiple FPGA program mapping and control. ...

[Heating apparatus for vehicle](#)

T Morikawa, H Ito, S Aoki, G Uchida, Y Kato, N ... - US Patent ..., 1999 - Google Patents

... 5. FIG. 9 is a flowchart of a segment of a program related to an engine ECU (electronic control
unit) in FIG. 5. FIG. ... 1, 3, and 4. FIG. 11 is a flowchart of a segment of a program related to an engine
ECU (electronic control unit) in a second embodiment of this invention. FIG. ...

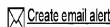
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[Pressure sensor using a pressure responsive magnetic film to vary inductance of a coil](#)

M Suzuki, F Murakami, Y Nakano, H ... - US Patent ..., 1994 - Google Patents

... 13 is a characteristic map showing an observed a signal component which has been ... 101 are
respectively connected deposition (CVD) and physical vapor deposition to an electronic control
unit (hereinafter referred ... like, other than by bonding as in "ECU") 35 via a lead wire or a ...

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[\[PDF\] Brake-by-wire without mechanical backup by using a TTP-communication network](#)[tuwien.ac.at \[PDF\]](#)

B Hedenetz, R Belschner - SAE transactions, 1999 - vmars.tuwien.ac.at

... A TTP controller **hardware** (see Figure 3) is supplied with an autonomous device called a ... the second chapter our main focus is to develop an **architecture** that guarantees ... purpose we used redundancy in sensors, power supply, electric components, **software**, and communication ...

Cited by 37 - [Related articles](#) - [View as HTML](#) - [BL Direct](#) - [All 3 versions](#)[AIL: description of a global electronic architecture at the vehicle scale](#)A Panday, D Couderc, S Marichalar - ... of the conference on **Design**, ..., 2001 - portal.acm.org

... This component provides a generic **software** layer suitable for all **hardware** architectures. ... Once the **architecture** for a given vehicle is established, fully described and physically placed, the compilation chain for each **ECU** freezes application **software** and middleware ...

Cited by 5 - [Related articles](#) - [All 2 versions](#)[\[PDF\] Holistic Object-Oriented Modelling of Distributed Automotive Real-Time Control Applications](#)[psu.edu \[PDF\]](#)

J Axelsson - Proceedings of the 2nd IEEE International Symposium ..., 1999 - Citeseer

... The big challenge is to combine the use of higher level implementations with small **hardware** resources. ... In the uppermost, functional, layer, the underlying **architecture** is not considered at all. Then, in the **software** layer, the same function has been partitioned, ie parts of it have ...

Cited by 16 - [Related articles](#) - [View as HTML](#) - [All 10 versions](#)[\[PDF\] Titus—A Graphical Design Methodology for Embedded Automotive Software](#)[unibw.de \[PDF\]](#)

U Freund, A Burst, EGH Stuttgart - Workshop on Object-oriented ..., 2000 - cs.unibw.de

... Being an **architecture** description language [5][8], the TITUS **design** methodology comprises **design** ... Since in automotive **software** resources are always allocated statically and dynamic instantiation is ... Client/server classes are independent from **hardware** and can be assigned to ...

Cited by 2 - [Related articles](#) - [View as HTML](#)[\[PDF\] Development support for the design of distributed control systems in a road vehicle](#)[210.101.116.115 \[PDF\]](#)

R Moser, J Wiedemann, U Honekamp - 2000 - 210.101.116.115

... prevents a target-specific implementation of the code from a new mapping of this behavior on a **hardware architecture**. ... The link between an **ECU-software** developing tool and a ... for further work in the domain of distributed control and leads to a more sophisticated system **design**. ...

[Related articles](#) - [View as HTML](#) - [All 3 versions](#)[\[citation\] Performance Evaluation of a Multiple Networked in-Vehicle Embedded Architecture](#)

Related articles

DESIGN AND VERIFICATION OF AUTOMOTIVE NETWORKED SYSTEMS USING ARCHITECTURAL SIMULATION

M BMW AG - IFAC proceedings series, 1984 - books.google.com

... When setting up a model of **software** or **hardware**, there will ... cover pure technical risks in terms of complexity handling, model transformation, simulation run time, ease of use, etc., and require changes in the development process to get the benefits of **architecture** simulation in ...

Related articles

Multi-Protocol LAN Design & Implementation: A Case Study

S Hazari - THE Journal (Technological Horizons In Education), 1995 - questia.com

... Changes to **network topology, hardware and software** must be documented and maintained on a continual basis. ... **Client/Server: Architecture** where computing responsibility is distributed between a front-end and back-end program. ...

Cited by 1 - Related articles - All 2 versions

Specification and design of electronic control units

psu.edu [PDF]

J Bortolazzi, T Hirth, T Raith - ... of the conference on European design 1996 - portal.acm.org

... Figure 4. Current **network architecture** for distributed vehicle control functions ... PC WindowsNT MatrixX STATEMATE AutoCode GNU C Compiler/Linker **Hardware** Connection Editor ... heavily demand the following improvements: • Library based reuse of functionality and **software** ...

Cited by 6 - Related articles - All 6 versions

Architectural trends in automotive electronics

U Kiencke, T Kytölä, KJ Neumann - Annual Reviews in Control, 1996 - Elsevier

... When the result of the analysis is finally positive, a more detailed **design** of the **hardware** and the **software** are performed separately ... Systems Hardware/software codesign may be applied either by a semiconductor supplier or a customer to determine the **LC architecture** for a ...

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[\[PDF\] Ptolemy: A framework for simulating and prototyping heterogeneous systems](#)[psu.edu](#) [PDF]

J Buck, S Ha, EA Lee, DG Messerschmitt - International Journal of ..., 1994 - CiteSeer
 ... pro- cessing **software**. An interesting distinction between MQ and many other domains
 is that graphical ... the Thor **hardware** timing simulator [32]). We are also beginning to
design a domain with finite-state- machine semantics. The ...
 Cited by 965 - Related articles - View as HTML - All 9 versions

[Java-based graphical user interface for MRUI, a software package for quantitation of in vivo/medical magnetic resonance
 spectroscopy signals](#)

A Naressi, C Couturier, I Castang, R De Beer, D ... - Computers in biology ..., 2001 - Elsevier
 ... Fig. 1. Directory/package **hierarchy** of the MRUI **software** system. ... Fig. 5. UML class **diagram** of
 mrui.quantitation.Amares.GUI.AmaresSeriesWin. View Within Article. ... In the Java-based
 MRUI-GUI **software**, described in this paper, the nomenclature preprocessing is used, however. ...
 Cited by 104 - Related articles - BL Direct - All 6 versions

[\[BOOK\] Refactoring: improving the design of existing code](#)[psu.edu](#) [PDF]

M Fowler, K Beck - 1999 - books.google.com
 ... from one class to another, pull some code out of a method to make into its own method, and push
 some code up or down a **hierarchy**. Yet the cumulative effect of these small changes can radically
 improve the **design**. It is the exact reverse of the normal notion of **software** decay. ...
 Cited by 3436 - Related articles - Library Search - BL Direct - All 35 versions

[\[CITATION\] Requirements analysis and system design](#)[mq.edu.au](#) [PDF]

L Maciaszek - 2007 - Pearson Education
 Cited by 232 - Related articles - Library Search - All 9 versions

[Model-integrated development of embedded software](#)[psu.edu](#) [PDF]

G Karsai, J Sztipanovits, A Ledeczi, T ... - Proceedings of the ..., 2003 - ieeexplore.ieee.org
 ... Circuits **Hardware** Description Language from the **hardware** models specifying the **hardware**
 architecture, and ... system, with a general-purpose editing engine, separate viewer-controller **GUI**,
 and a ... the composed designs are crucial in model-based system/**software** development ...
 Cited by 306 - Related articles - BL Direct - All 24 versions

[UML-based multiprocessor SoC design framework](#)[psu.edu](#) [PDF]

T Kangas, P Kukkala, H Orsila, E Salminen, ... - ACM Transactions on ..., 2006 - portal.acm.org
 ... A screenshot of the **GUI** is shown in Figure 3. With Koski **GUI**, it is ... The library also contains both

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- 2001

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[\[PDF\] A new approach to functional and software structure for engine management systems-BOSCH ME7](#)[funpic.de \[PDF\]](#)

J Gerhardt, H Hönninger, H Bischof - SAE transactions, 1998 - cloudy.d.funpic.de

... unit in the early 70's, injection and ignition were integrated into one single **electronic control unit** during the ... The introduction of electronic throttle control (ETC) as a drive-by-wire system with it's ... timing and cylinder individual fuel cut-off could be described in a five dimension **map**. ...

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R Moser, R Wiedemann, U Honekamp - 2000 - 210.101.116.115

... Keywords: code-generation, **electronic control unit**, **software** development tools ... which intend to replace mechanical solutions by electronic ones, eg drive/brake by wire, enforce this ... the other hand prevents a target-specific implementation of the code from a new **mapping** of this ...

[Related articles](#) - [View as HTML](#) - [All 3 versions](#)[High performance hybrid micro-computer](#)

KG Blemel - US Patent App. 09/977,140, 2001 - Google Patents

... These improvements do not require alternative **wiring** harness changes and do not affect ... micro-controller 8. Program addresses are accomplished by an address **partitioning** and control ... with slave serial and selectMAP functions for multiple FPGA program **mapping** and control. ...

[\[PDF\] THE UNIVERSITY OF CALGARY](#)[ucalgary.ca \[PDF\]](#)

R Mehrotra - 1998 - dspace1.acs.ucalgary.ca

... TBI TDC TWC Air-fuel ratio (mass basis) Stoichiometric air-fuel ratio Bottom dead center Drive-by **wire Electronic control unit** Exhaust gas ... tables to closed-loop computer based diagnostic reflects the ongoing research effort and improvement m both **hardware** and **software**. ...

[Related articles](#) - [All 3 versions](#)[The Specification Language SpecC within the PARADISE Design Environment](#)[psu.edu \[PDF\]](#)

A Rettberg, FJ Rammig, A Gerstlauer, DD ... - ... and Design of ..., 2001 - books.google.com

... Hence, CHaRy supports the **mapping** of controller models to a number of tasks (**partitioning**), the extraction of their computation times (timing analysis), and the assignment of tasks to a processor network ... In IEEE Colloquium: **Partitioning in Hardware-Software Codesings**. ...

[Cited by 5](#) - [Related articles](#) - [All 10 versions](#)

System for controlling the output power of a motor vehicle

M Itoh, K Yamada, H Yoshida, K Ohtake, Y ... - US Patent ..., 1993 - Google Patents

... The motor vehicle output power control system includes an **electronic control unit** for controlling an actuator to lower the output drive torque of the engine, independent ... Ada 9X Program Structure and Compilation Issues, Draft Mapping Document, Feb. ... "System Partitioning and Its ...

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Session 1A

LE Porter, W Merrill - ... on Industrial & Engineering Applications of ..., 1988 - books.google.com

... 3. The **mapping** of the state to hypotheses is neither unique nor one-to-one **mapping**. ...

Test-Object A test-object describes a test to be performed such as taking a voltage reading at a test point, or visually inspecting the connection of a piece of wire. ...

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by A Rajnak - Cited by 4 - Related articles

architecture design, **scheduling of networks** and **tasks** distributed across multiple ECUs, **harness design** **hardware interfaces** are specified (e.g. CPU type, communication ... configuring and building the **software** image for a particular ECU. ... content of variants, and the **distribution** of options. Factors ...

videos.dac.com/44th/papers/31_4.pdf

2. PDF Open **Architecture** Solution for **Hardware**-in-the-Loop Testing

File Format: PDF/Adobe Acrobat - Quick View

test of the J1939 **network**, actual ECU **hardware** must be ... cabling and **harnesses**, and fabrication complexity ... One of the early **design** decisions facing the team was ... **interface** options, however the **software** was less well in a synchronous manner by **scheduling** each of the messages on the bus. ...

www.mathworks.com/mason/tag/proxy.html?dataid=11240&fileid...

3. **Design software** - engineering **software** | process **software** ...

water, sewer and stormwater **network design software** Bentley Systems Europe B V + ECU **Software** + Process Management + Solutions for... + **Hardware** ... The convenient and easy-to-operate user **interface** of the **Network Designer CAN** ... to interactively **design** the FlexRay **schedule** forms the basis for the tool. ...

www.directindustry.com/.../design-software-77666.html

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4. Autosar standards open the door to auto **design** optimization, Part 1

Oct 1, 2009 ... The emerging Autosar automotive **design software**

standard began as the product ... Autosar SWCs interact via an application programming **interface** (API) known as ... and might even add cost to other components such as the wiring **harness**. ... and it knows the throughput of the ECU's **network** controller. ... www.eetimes.com/design/...design/.../Autosar-standards-open-the-door-to-auto-design-optimization-Part-1 - Cached

5. [\[PDF\] AUTOSAR Basic **Software** for Complex Control Units](#)

File Format: PDF/Adobe Acrobat - Quick View

Architecture) has been a de facto standard for **software** ... automotive ECU **networks** is to use domain controllers. A ... **distribution** of control functions, they also offer new ... In addition to **hardware** safety, the ECU-**software** has also ... **Hardware interfaces** like EB's own EB 61x0 are then used ... www.date-conference.com/proceedings/PAPERS/2010/.../03.8_3. PDF

6. [COMPLEXITY MANAGEMENT FOR VEHICLE ELECTRICAL/ELECTRONIC ...](#)

the **harness** (or wiring) domain responsible for **designing** the wire **harness** To illustrate, the ECU **design** data 219 may be formatted for use by a The **network interface** device 320 can provide connectivity to a **network** 326, e.g., repartitioning the **distribution** of tasks and functions between **hardware** ... www.faqs.org/patents/app/20100031212 - Cached - Similar

7. [ETAS - eNewsletter, Issue 1/2010 - United Kingdom & Nordic ...](#)

Jun 30, 2010 ... New ECU and bus **interface** modules. The new compact **hardware** modules of the ... A new approach for process-oriented and tool based automation of calibration tasks ... Thanks to the open **architecture** LABCAR customers are able to ... CLONE RTPC in RTPC Web **Interface** for easy **distribution** of RTPC set up ... www.etas.com/en/etas_uk_contact.php?regionalPage=/regional/... - Cached

8. [\[PDF\] Microsoft PowerPoint - Workshop ETFA09 Navet](#)

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by N Navet - 2009 - Related articles

Sep 25, 2009 ... wiring **harness** is tailored to the options. Picture

from [10] ... Optimizing the use of **networks** is becoming an ... Intra- and inter-ECU Communication. MW hides the **distribution** ... But **task scheduling** has to be adapted otherwise [5] AUTOSAR – an open standardized **software architecture** for the ...

www.realtimeatwork.com/wp-content/...

Workshop_ETFA09_Navet_bw.pdf

9. [A Conceptual Data Model for the **Architecture** Exploration of ...](#)

by P Giusto - 2007 - Related articles

The **design** of in-vehicle **distributed** embedded ... definition of **software task** execution **architectures** and the ... Analysis. Wiring **Harness. Design. Software-. Hardware. Verification** and.

Detailed has only a functional **interface**, an ECU owns both a ...

periodic vs. data-driven) – the bus **schedule** may be not ...

ieeexplore.ieee.org/iel5/4296570/4296571/04296683.pdf?amumber...

10. [Adam Elbirt - LinkedIn](#)

Greater Boston Area - Digital Systems Group Leader and Senior Member of Technical Staff at Draper Laboratory

Computer **architecture**, digital system **design**, real-time and embedded systems, ... **scheduling** and budgeting of all **hardware** development **tasks** and assigned personnel, ... Member of the

Network & Systems Security (NSS) Laboratory within the **Tasks** included Host to ECU **software interface** modification and command ...

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